

Abstracts

A novel clock recovery circuit for fully monolithic integration

K. Murata and T. Otsuji. "A novel clock recovery circuit for fully monolithic integration." 1999 MTT-S International Microwave Symposium Digest 99.1 (1999 Vol. 1 [MWSYM]): 201-204 vol. 1.

This paper presents a novel clock recovery circuit that offers fully monolithic integration. The circuit consists of just the transistor gates of an exclusive OR for an edge-detector and a free-running T-type flip-flop for a high-Q oscillator. As the first proof of the concept, we assemble a circuit using discrete ICs and confirm half-rate clock extraction from an NRZ data stream input.

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